

Fig. 16

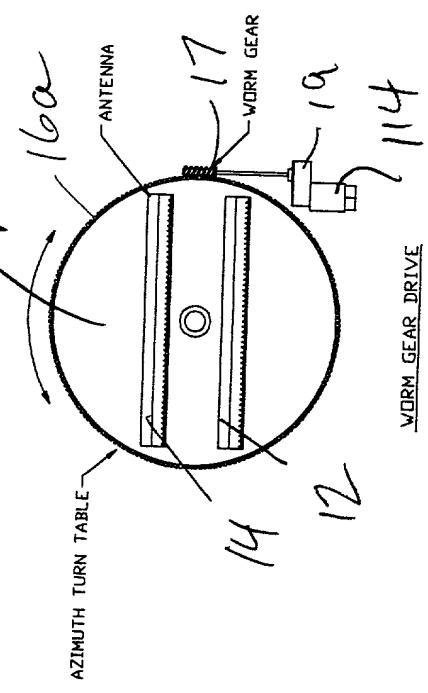


Fig. 1a

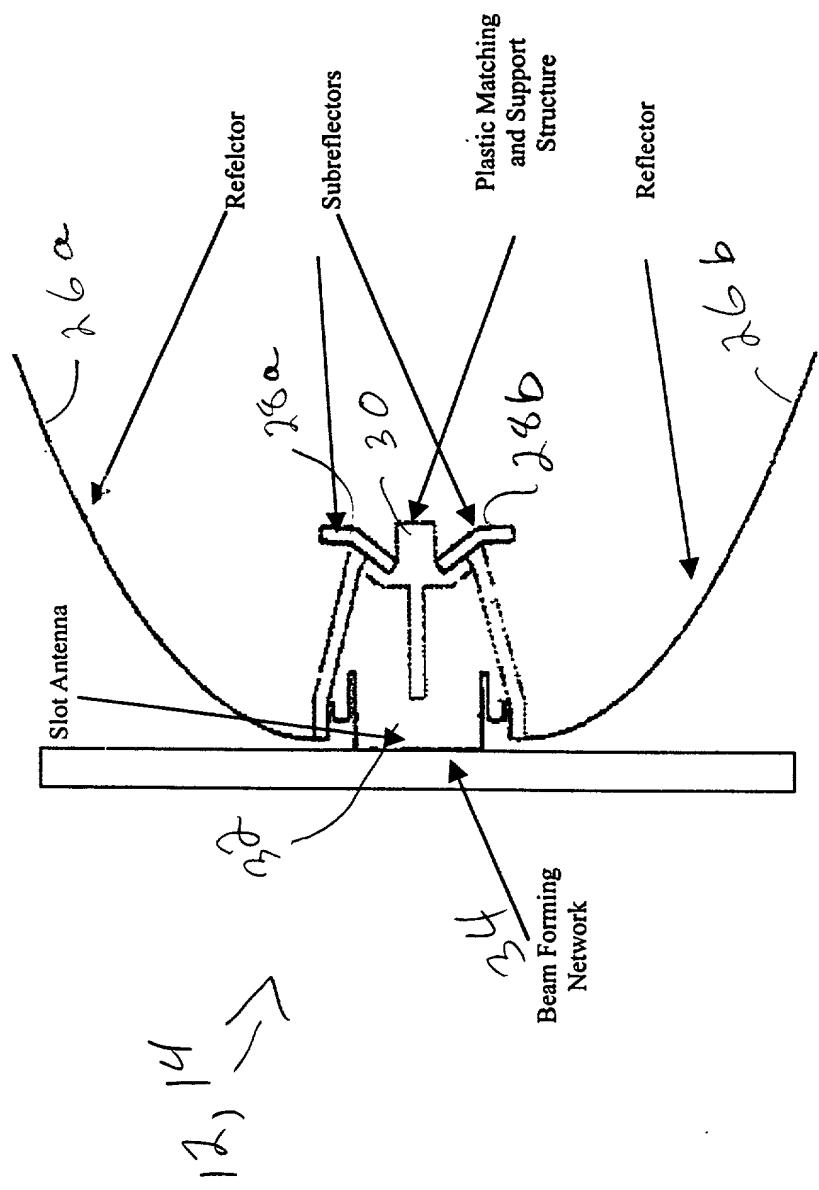


Fig. 2

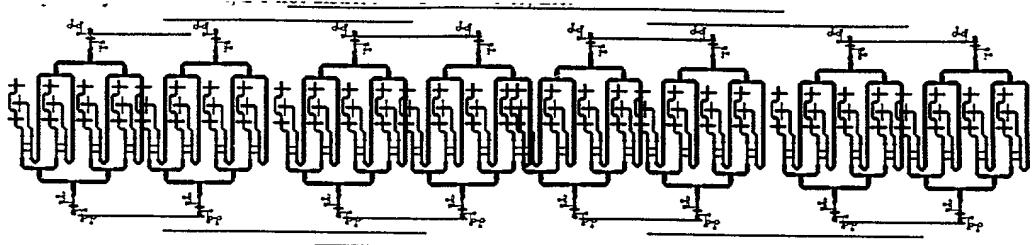


Figure 3

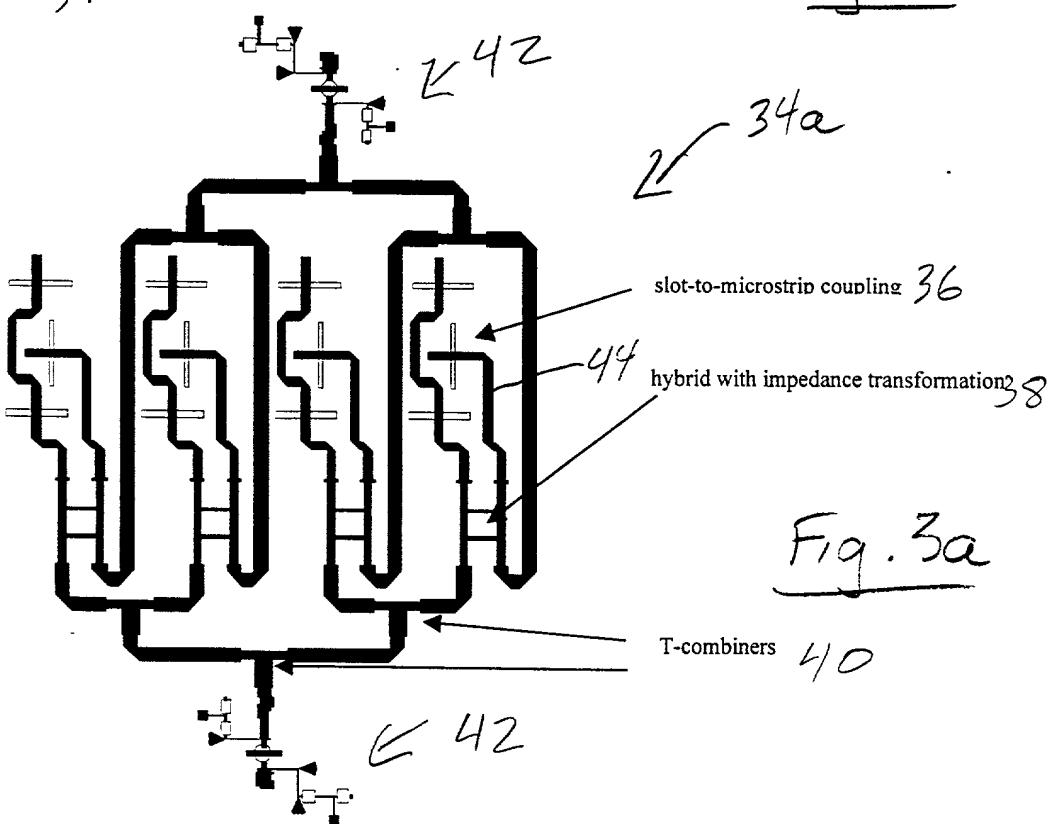
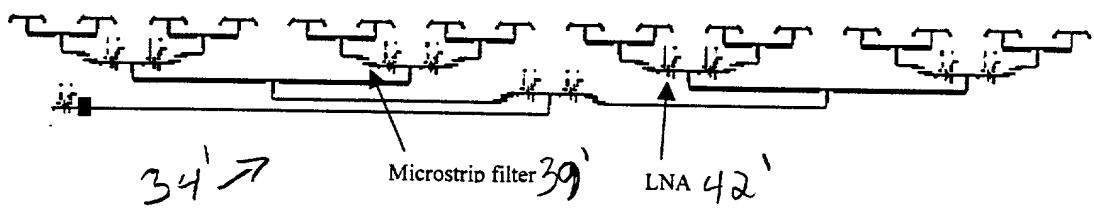


Fig. 3

34a



34'

Microstrip filter 39'

LNA 42'

Fig. 6

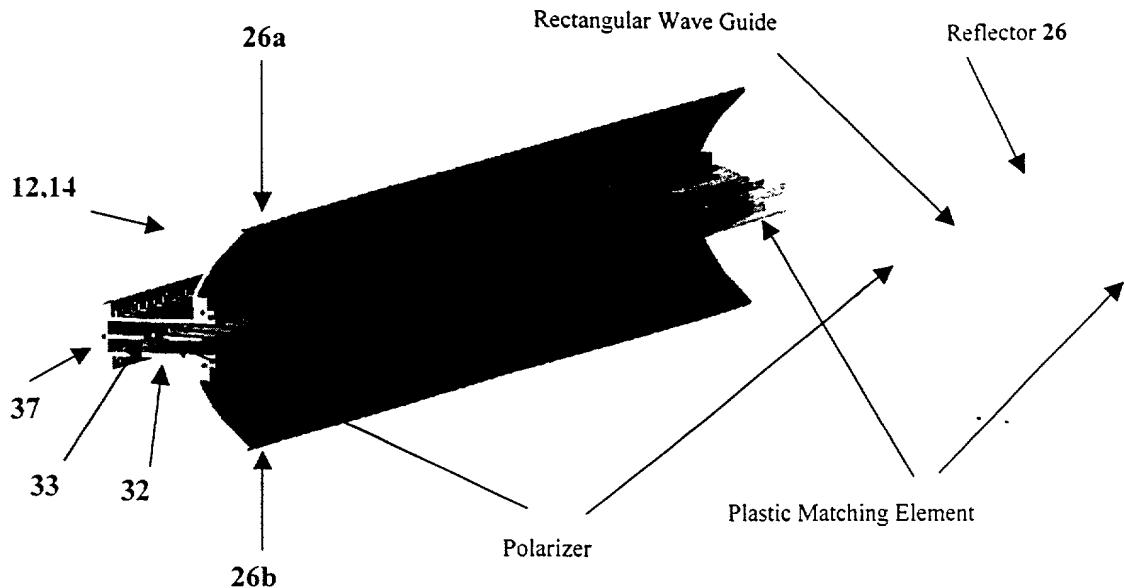


Figure. 4

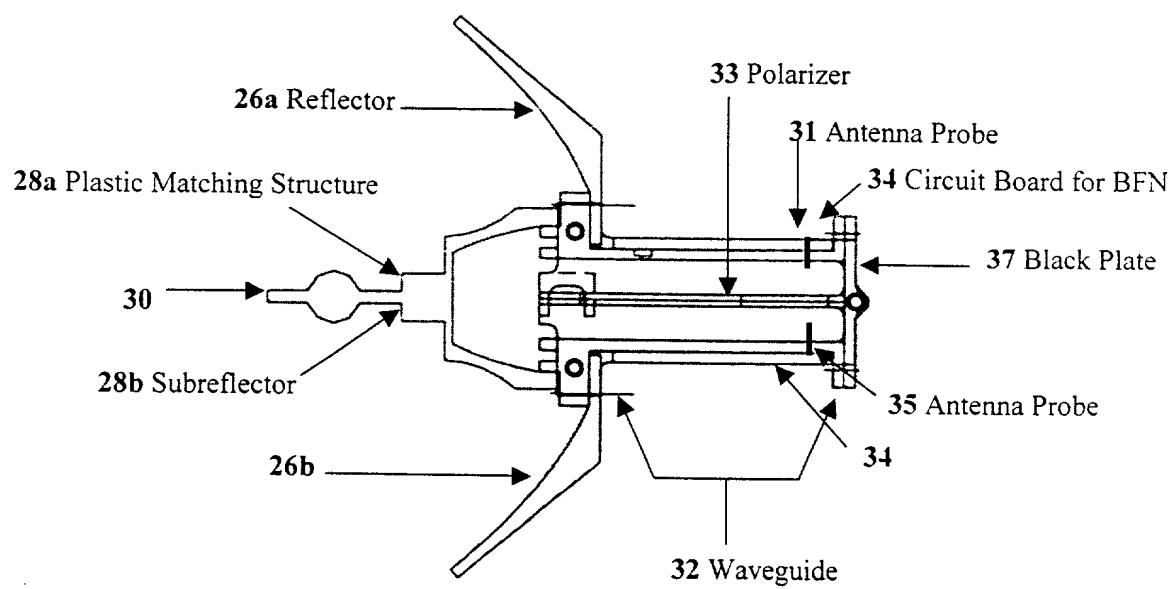


Figure. 5

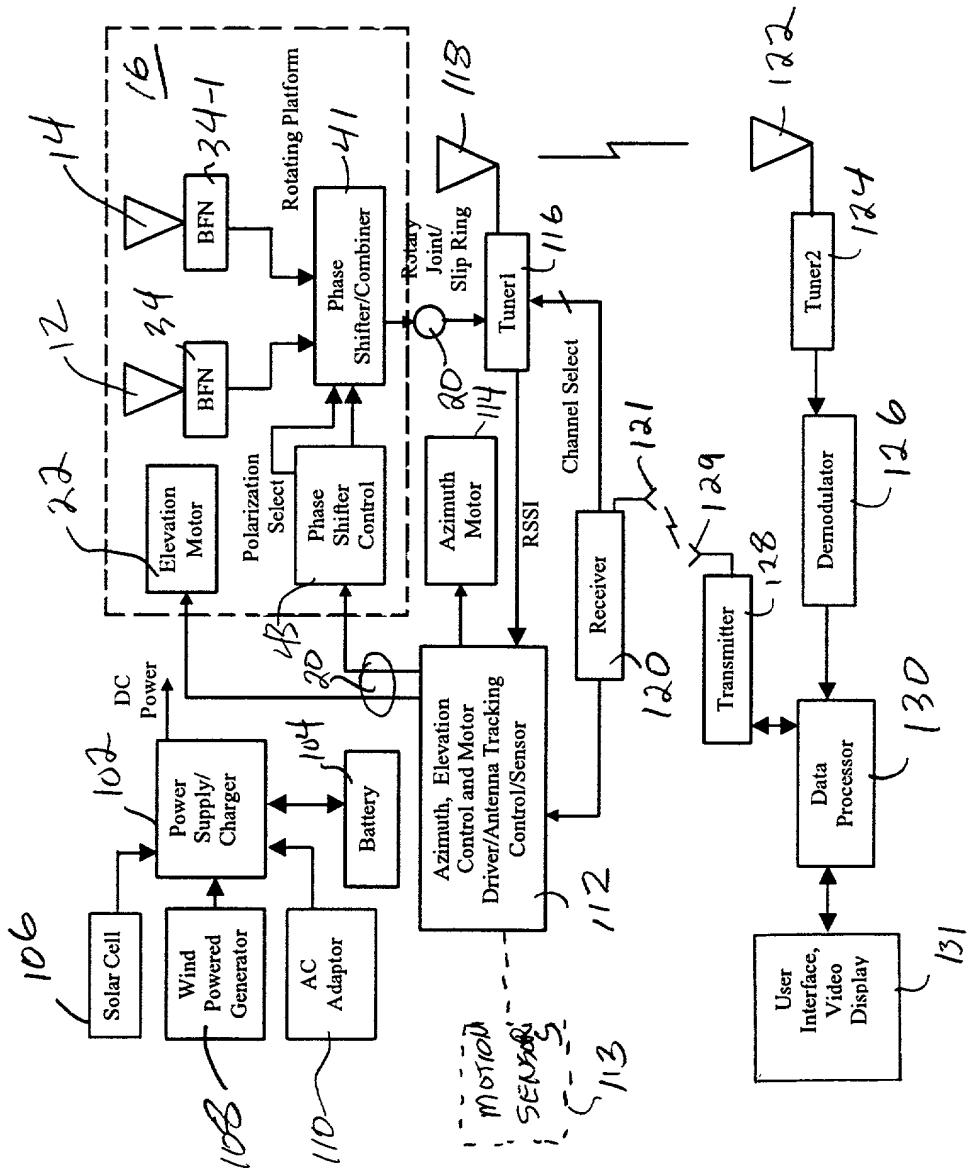


Fig. 7

Flow Chart for Data Port Interface

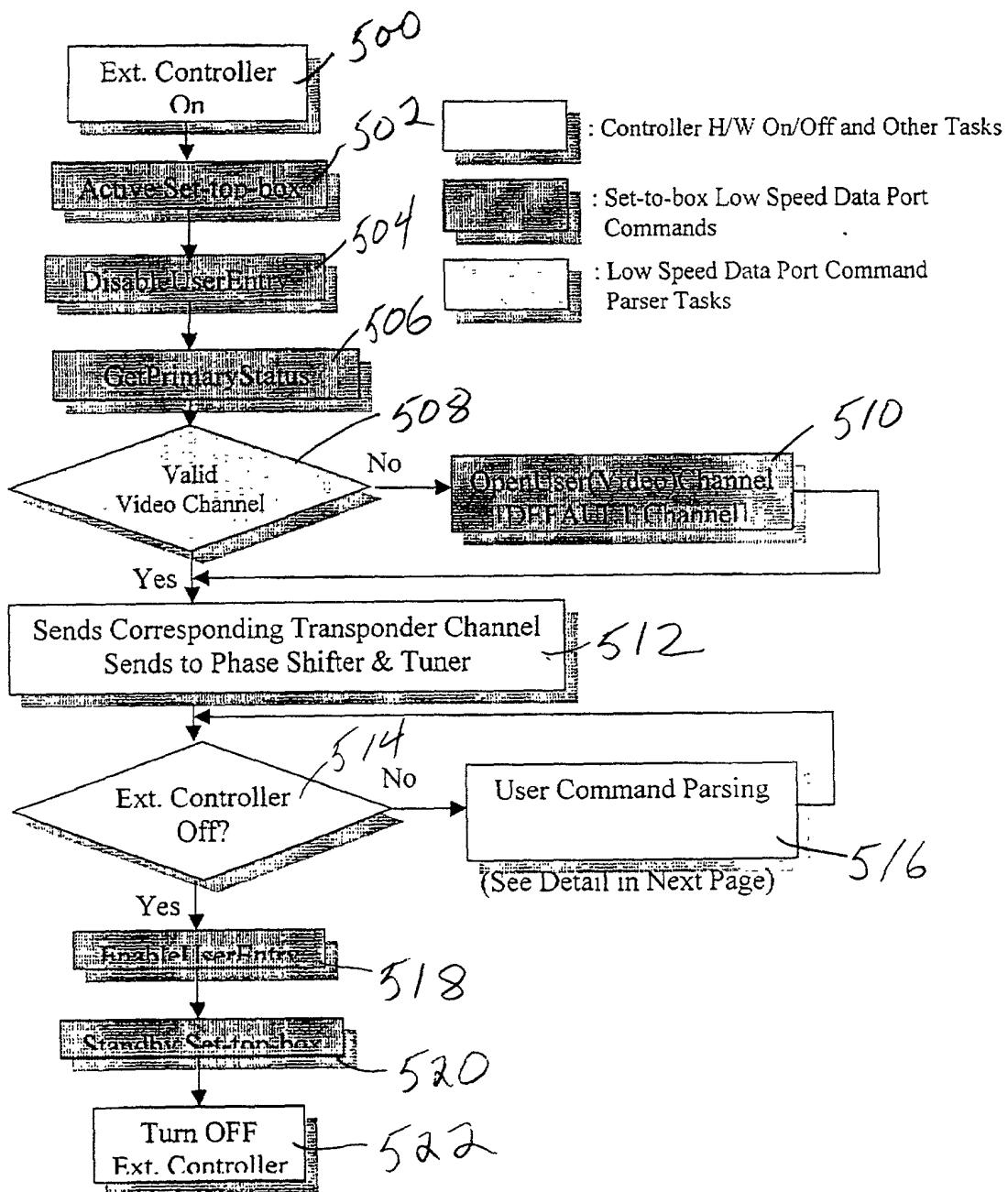


Fig. 7a

Flow Chart for
Data Port Interface User Command Parsing

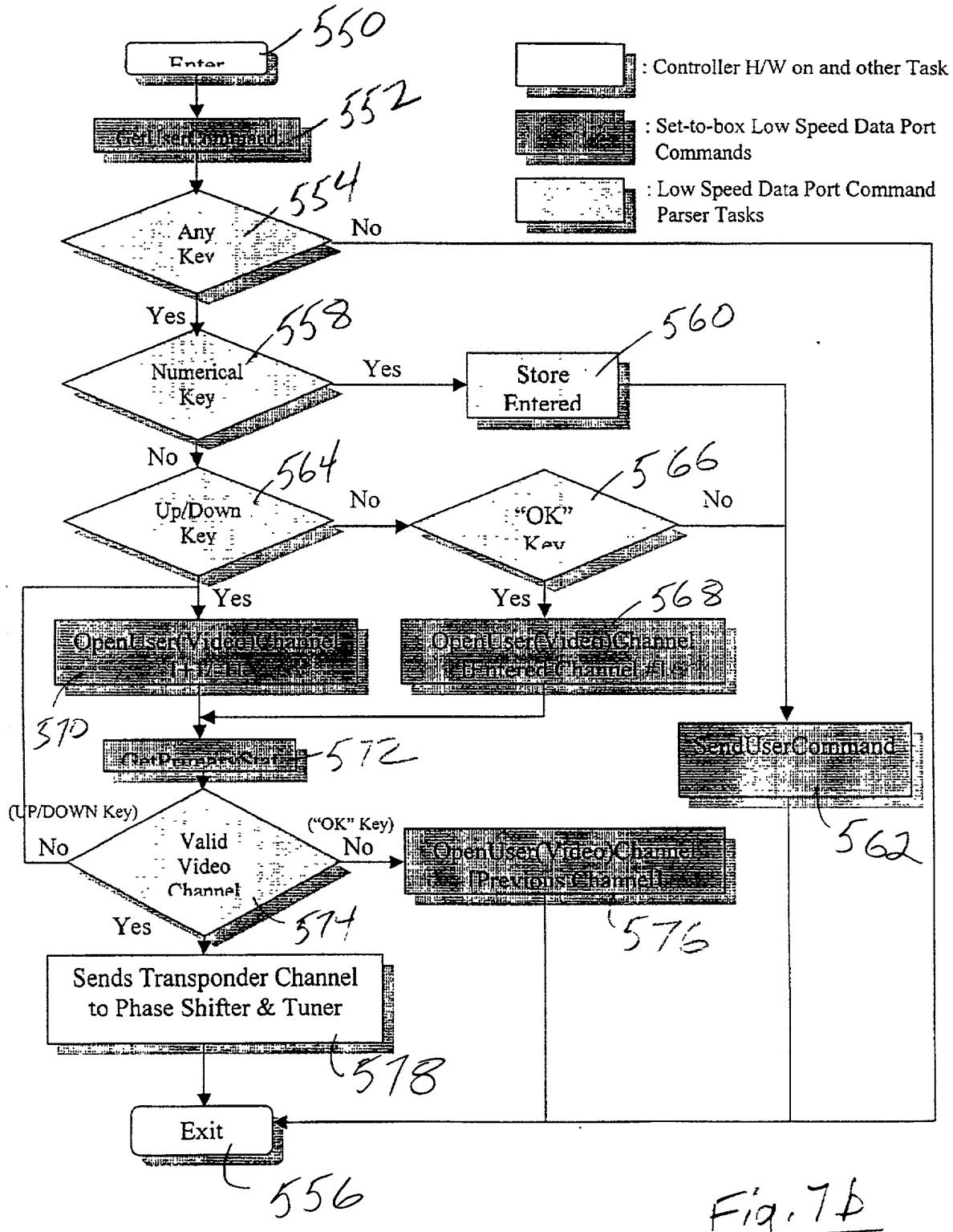


Fig. 7b

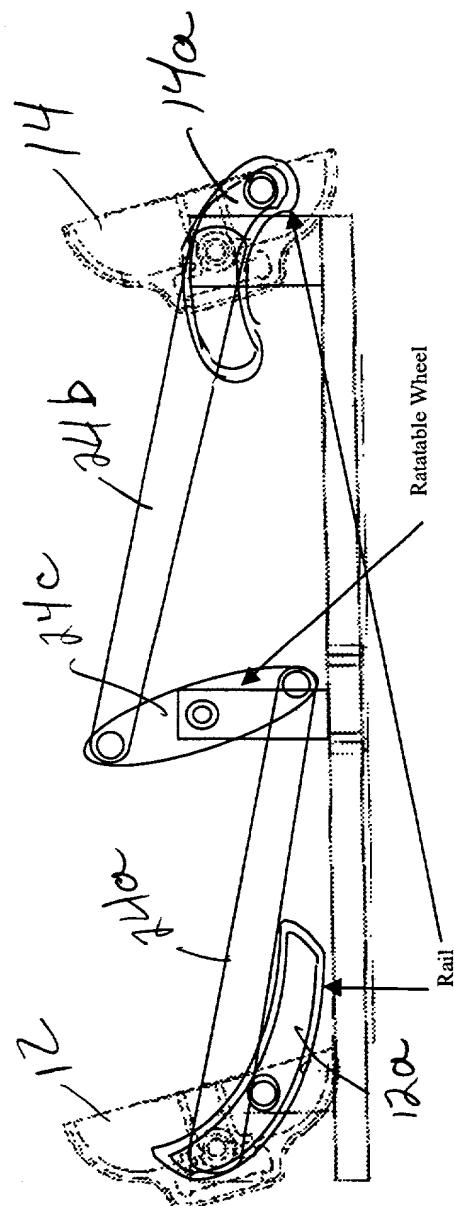


Fig. 8

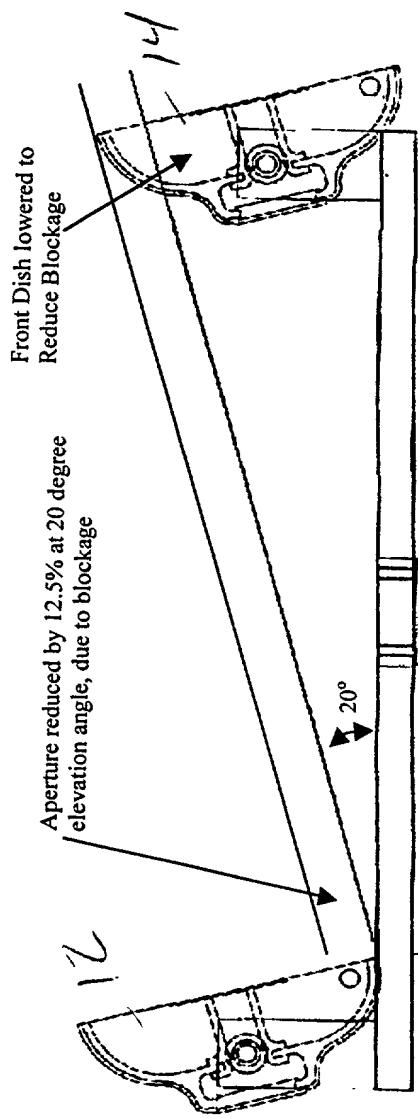


Fig. 9

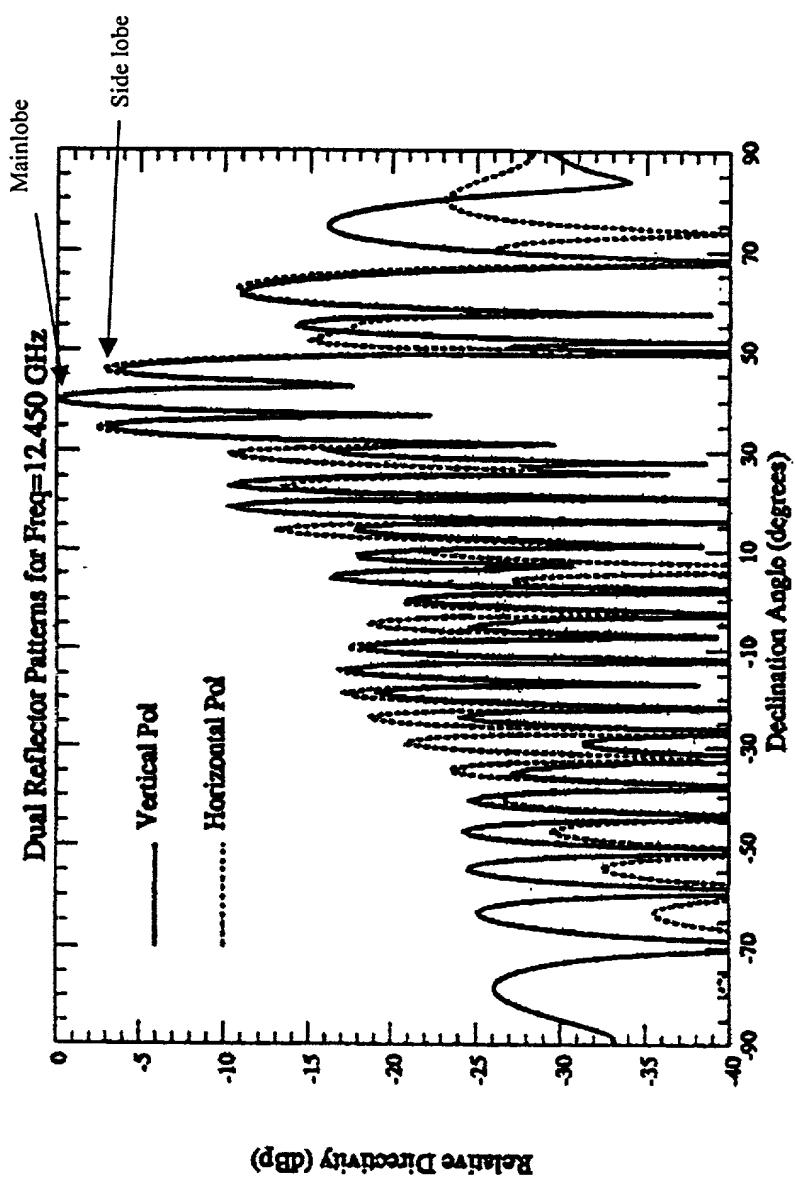


Fig. 10

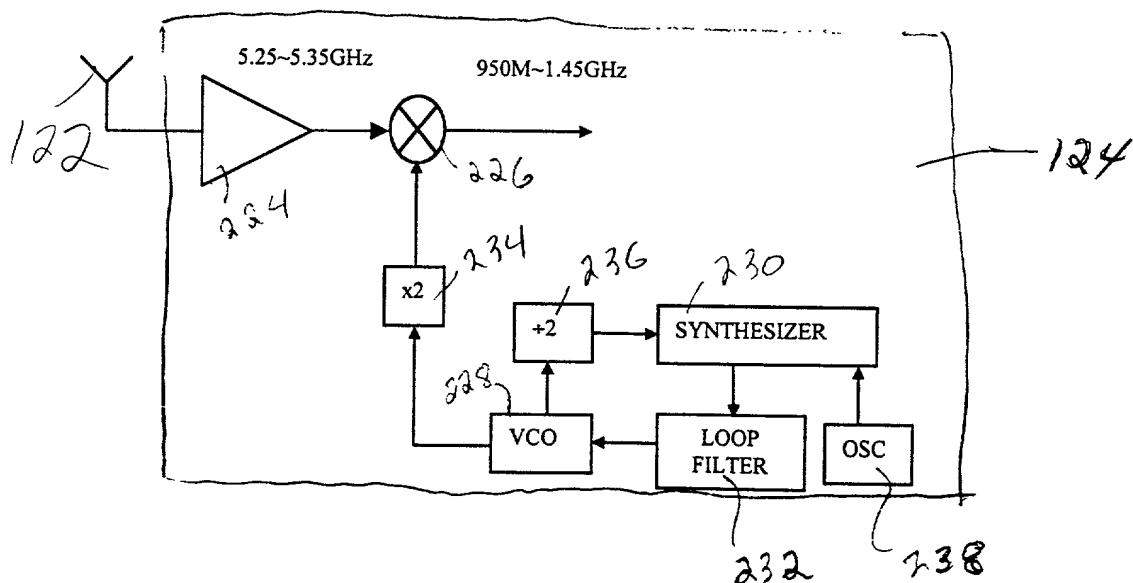
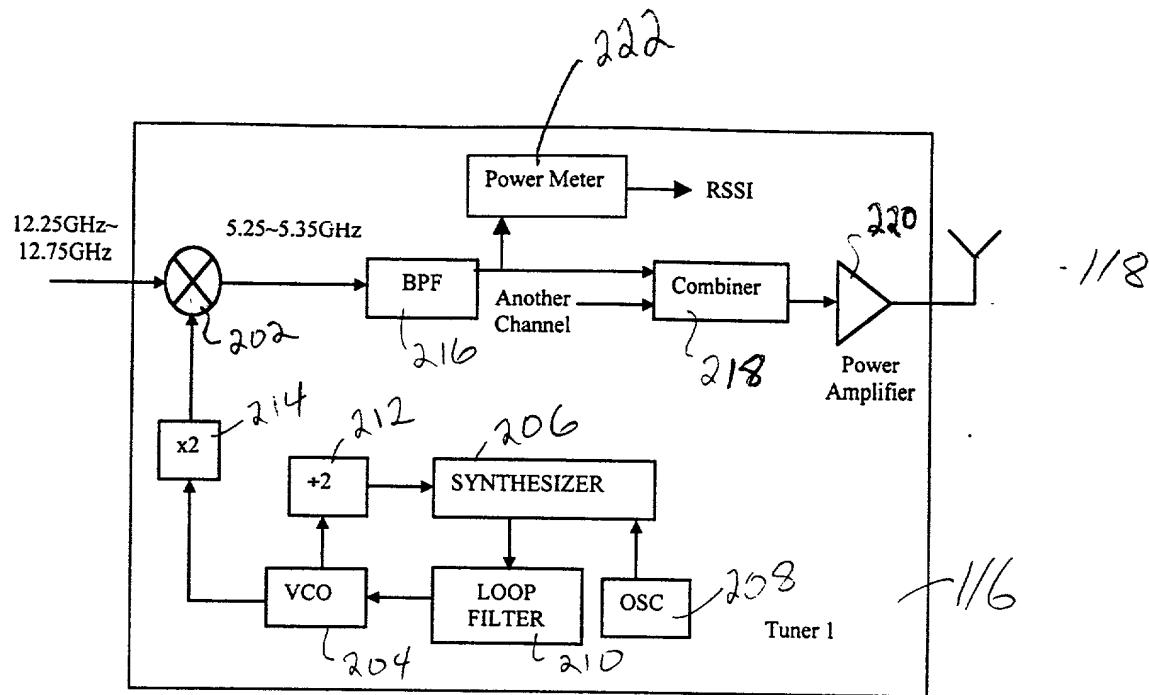


Fig. 11

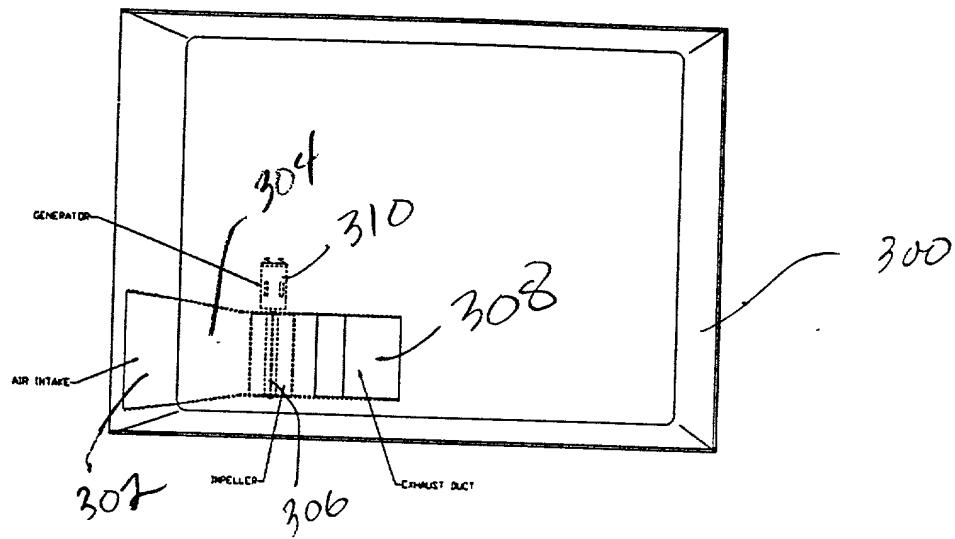


Fig. 12

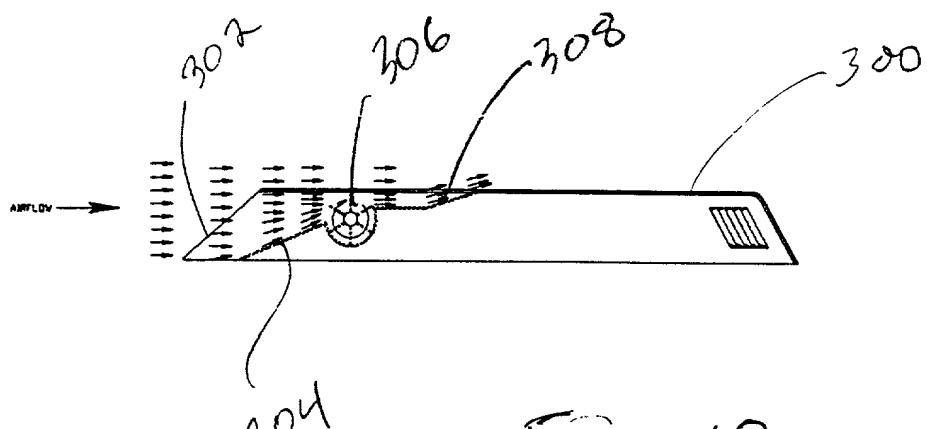


Fig. 13

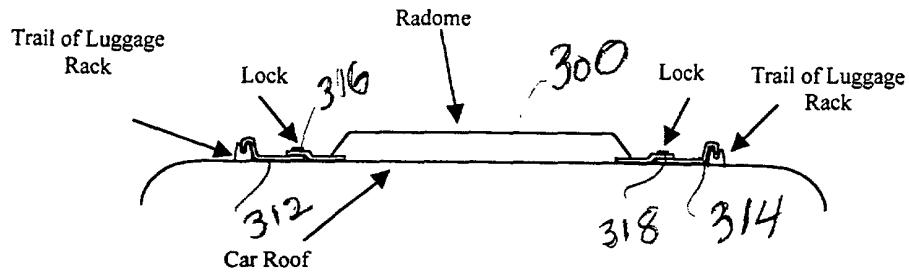


Fig. 14

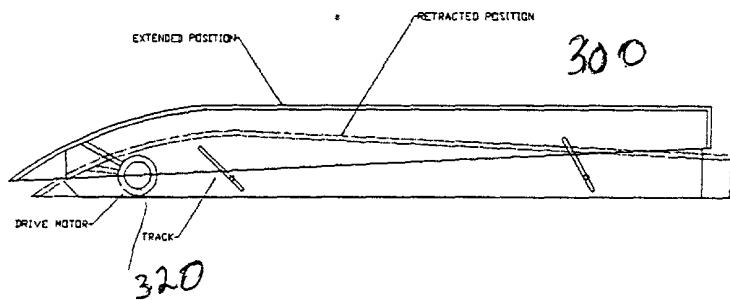


Fig. 15

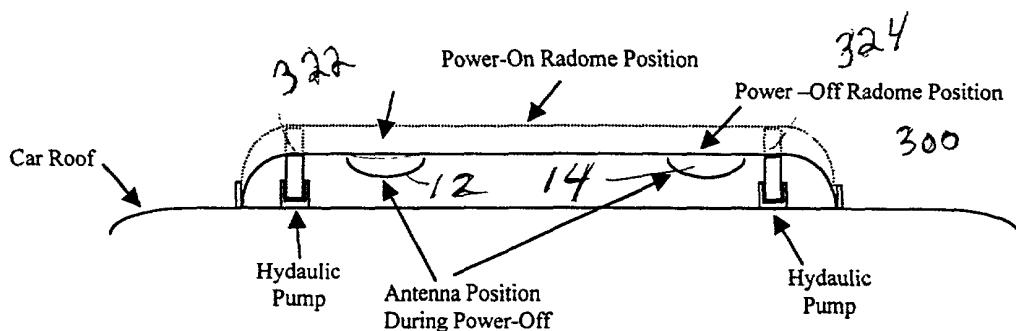


Fig. 16